

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

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Applicant(s)

Klaas Bult, et al.

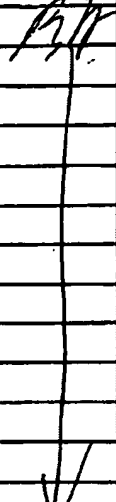
Group Art Unit

2819

Examiner Name

To be assigned

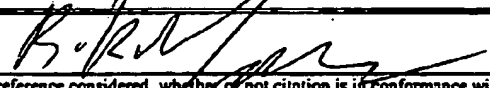
U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - Kind Code <sup>2</sup> (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
		4,905,189	02-1990	Brunolli
		5,105,193	04-1992	Vogt et al.
		5,689,257	11-1997	Mercer et al.
		5,825,317	10-1998	Anderson et al.
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		US 6,191, 719 B1	02-20-2001	Bult et al.
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		US 6,414,618 B2	07-02-2002	Bult et al.
		US 6,522,279 B2	02-18-2003	Bult et al.
	US 6,714,150 B2	03-30-2004	Bult et al.	

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
Bult		Article entitled "An 8-bit 2-ns Monolithic DAC", IEEE Journal of Solid-State Circuits, Vol. 23, No. 1, Feb. 1988, pp. 142-146
Bult		Article entitled "a 27-MHz Digital-to-Analog Video Processor", IEEE Journal of Solid-State Circuits, Vol. 23, No. 6, Dec. 1988, pp. 1358-1369
Bult		Vogt, Alexander W. et al., Article entitled "A 10-Bit High Speed CMOS DAC Macrocell", 1989, pp. 6.7.1-6.7.4

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EXAMINER SIGNATURE		DATE CONSIDERED	11/24/04
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